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Patent  
Attorney's Docket No. 026125-068

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Jiren YUAN

Application No.: 09/672,803

Filed: September 28, 2000

For: Versatile Charge Sampling Circuits

Group Art Unit: 2816

Examiner: My Trang Ton

Confirmation No.: 8153

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J. H.  
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AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Examiner's Action mailed on March 27, 2002, please  
amend this application as follows.

In the Claims

Please **CANCEL** claims 54-57.

Please **REPLACE** claims 34, 38, 40, and 44 with the following amended  
claims:

*D1 Sub G1*  
~~34.~~ (Amended) The charge sampling circuit according to claim ~~32~~, wherein the  
control signal generator controls the integrator to hold the sample until a resetting  
signal from the generator is applied to a control input of the integrator.

*D2 Sub G1*  
~~38.~~ (Amended) A band-pass charge sampling circuit, comprising:  
a control signal generator for controlling a first and second portion of a  
differential analog signal;  
a first signal input for receiving the first portion of the differential analog signal;  
a second signal input for receiving the second portion of the differential analog  
signal;  
an integrator; and  
a weighting-and-sampling element for processing the differential analog signal  
during a weighting-and-sampling phase responsive to a weighting-and-sampling  
signal from said control signal generator, wherein a current of said differential analog